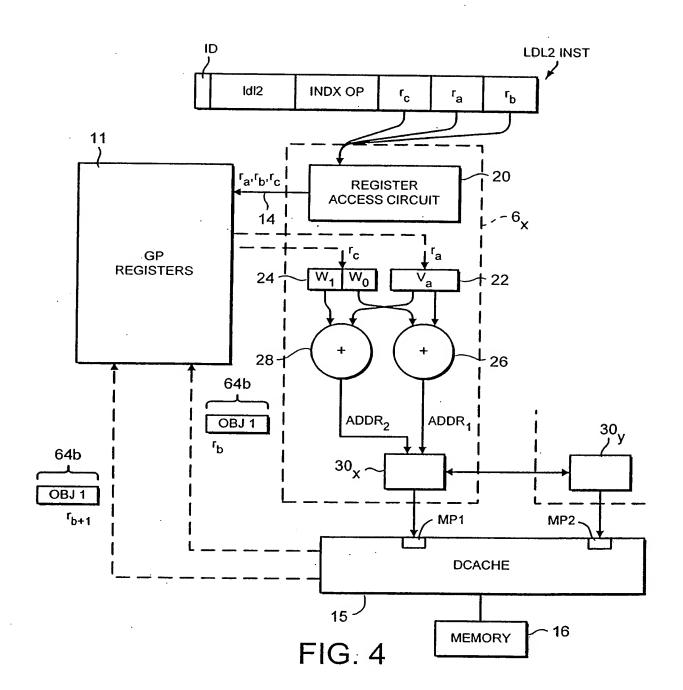


F G 3

ID BIT

1	<u>E</u>	(2)	(3)	<u>(4)</u>	(5)	(9)	<u>(7</u>
05 04 03 02 01 00	DST	DST	SIGN   M REG	TSO	TSO	DST	DST
11 10 09 08 07 06	SRC1	SRC1	SRC1	SRC1	SRC1	BASE REG	BASE REG
22 21 20 19 18 17 16 15 14 13 12 11 10 09 08 07 06 05 04 03 02 01 00	SRC2	IMMED	SRC2	SHIFT   M REG	SRC2	MMEDIATE OFFSET	INDEX REG
21 20   19   18	TST	TST	TST	TST	TST	IMMEDIAT	NDX OP T
24   23   22   2			0)	<u>()</u>	<u>()</u>	— <u> </u>	<u>_</u>
31 30 29 28 27 26 25 24 23	OPCODE			OPCODE	OPCODE	OPCODE	OPCODE
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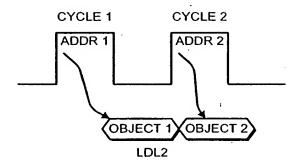


FIG. 5a

